IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Cheng, et al.

Docket No.:

TSM02-0971

Filed:

Herewith

Examiner:

TBD

Serial No.:

TBD

Art Unit:

TBD

For:

Method for Reducing Defects in Post Passivation Interconnect Process

Mail Stop: Patent Application Commissioner for Patents P. O. Box 1450

Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Applicant wishes to bring to the attention of the Patent and Trademark Office the information noted on the enclosed form PTO/SB/08A that may be considered material to the examination of the above identified application.

No fee is due at this time, as this Information Disclosure Statement is being filed concurrently with the patent application.

August 6, 2003

Date

Steven H. Slater

Attorney for Applicant

Respectfully submitted,

Reg. No. 35,361

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PTO/SB/08A (02-03)

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Substitute for form 1449/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Complete if Known		
				Application Number	TBD	
				Filing Date	Herewith	
				First Named Inventor	Cheng, et al.	
				Art Unit	TBD	
(use as many sheets as necessary)			sary) 	Examiner Name	TBD	
Sheet	1	of	1	Attorney Docket Number	TSM02-0971	

U.S. PATENT DOCUMENTS						
Examiner Initials*	Cite No.1	Document Number Number - Kind Code² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
	Α	US-6,165,886	12-26-2000	Lin, et al.		
	В	US-6,455,885 B1	09-24-2002	Lin		
	С	US-6,495,442 B1	12-17-2002	Lin, et al.		
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FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite	Foreign Patent Document Country Code ³ - Number ⁴ - Kind Code ⁵ (<i>if known</i>)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T
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Examiner	Date	
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language Translation is attached.

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